PICAXE-18M2+ Update.

Introduction:

The original 18M2 part was the first part designed in the M2 series and its silicon was therefore initially designed over 2 years before the later 14M2/20M2 parts. With advances in technology these two later parts had some minor additional features not included within the original 18M2 silicon.

The 18M2 silicon has now been upgraded to also include these extra features. The new part is marked PICAXE-18M2+ and is known as the 18M2+ part. A surface mount version of the 18M2+ is now also available.

All programs and circuits designed for the older 18M2 will work immediately with an 18M2+ as features have only been improved; no existing feature has been removed.

Pinout:

PICAXE-18M2

(DAC / Touch / ADC / Out / In) C.2	1	18	C.1 (In / Out / ADC / Touch)
(SRQ / Out) Serial Out / C.3	2	17	C.0 (In / Out / ADC / Touch)
(In) Serial In / C.4	3	16	C.7 (In / Out) {kb data}
(In) C.5	4	15	C.6 (In / Out) {kb clock}
0V	5	14	+V
(SRI / Out / In) B.0	6	13	B.7 (In / Out / ADC / Touch)
(i2c sda / Touch / ADC / Out / In) B.1	7	12	B.6 (In / Out / ADC / Touch / pwm)
(hserin / Touch / ADC / Out / In) B.2	8	11	B.5 (In / Out / ADC / Touch / hserout)
(pwm / Touch / ADC / Out / In) B.3	9	10	B.4 (In / Out / ADC / Touch / i2c scl)
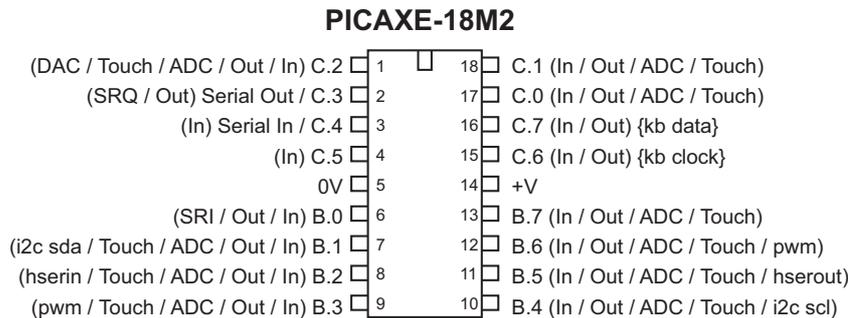
Improved Features:

Feature:	18M2+	18M2
Memory Capacity (bytes)	2048	2048
Total RAM (bytes)	512	256
Data EEPROM (bytes)	256 (separate)	256 (shared)
Parallel multi-tasking (starts)	8	4
Keyboard Support (kbin, kbled)	✔	✘
RF Support (rfin, rfout)	✔	✘
Table memory (table, readtable)	512	✘
Internal Temperature Sensor (readinternaltemp)	✔	✘
Label on Chip	PICAXE-18M2+ I/P	PICAXE-18M2 I/P
Firmware Version	D.A	2.A to 2.D

Software Version:

To use the new 18M2+ parts you must upgrade your software to this version (or later)

Programming Editor	5.4.3
Logicator	3.6.0
AXEpad	1.4.0

All updates are free to download from **www.picaxe.com/software**